

SEP 20 2006

501.32049RV1/329201392US4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yujiro KAJIHARA *et al.*

Serial No.: 09/987,978

Filed: 16 November 2001

For: LEAD FRAME SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE, USING THE SAME, AND METHOD
OF AND PROCESS FOR FABRICATING THE TWO

Art Unit: 2815

Examiner: J. Clark

Conf. No.: 7905

PETITION FOR EXTENSION OF TIME

Commissioner for Patents
POB 1450
Alexandria, Virginia 22313-1450

20 September 2006

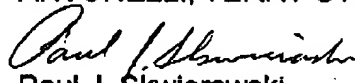
Sir:

In the matter of the above-identified application, applicants hereby respectfully petition for an extension of time to permit filing a response within the third month subsequent to expiration of the shortened statutory period set in the outstanding Office Action mailed March 20, 2006.

It is respectfully requested that any shortage in the fee be charged to the account of Antonelli, Terry, Stout & Kraus, LLP, Account No. 01-2135 (Case No. 1374.32049RV1).

Respectfully submitted,

ANTONELLI, TERRY STOUT & KRAUS, LLP


Paul J. Skwierawski
Registration No. 32,173

PJS/slk

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